



A New Approach for Detecting Memory Errors in JPEG2000 Standard

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Abstract—The rapid growth of image compression has dramatically increased to reduce memory size without degrading the quality of the image. This paper provides adaptive memory compensation errors in JPEG2000. Furthermore, we use algorithm-specific techniques such as DWT to compensate memory consumption and propose Huffman coding, MQ-Coding for compensating the memory errors without the need of tile memory. The Huffman coder can be used to compress and store large amount of data in a memory. These techniques do not require any additional memory can minimize the memory requirements having the less computation complexity with larger area and power consumption. The proposed architecture is used to increase the image quality as well as the compression rates. By this method, memory compensation and memory errors can be reduced. The image quality can also be increased with the help of Discrete Wavelet Transform.

Index Terms—JPEG; JPEG2000; MQ-CODER; SRAM errors; adaptive error control coding

1. INTRODUCTION

JPEG and JPEG2000 are the most widely used image compression standards in compensating memory errors. JPEG has slightly less compression performance than JPEG2000 [1]. JPEG is based on DCT whereas; JPEG2000 is based on DWT where each sub-band is divided into rectangular blocks, called code-blocks. DWT provides less computational complexity which can compensate memory errors drastically [2]. JPEG2000 outperforms JPEG in terms of compression ratio. JPEG2000 algorithm produces excellent results, better image quality as compared to JPEG [3]. Set partitioning in hierarchical trees (SPHIT) is also most widely used compression algorithm. It can also be combined with DCT and DWT for higher compression efficiency. It provides good image quality but cannot compensate the memory errors [4]. Block truncation coding (BTC) algorithm were also used for colour image compression which also provides good image quality cannot reduce memory errors [5]. Hence, JPEG2000 will be effective to operate SRAM under low-power mode which is a DWT based image compression standard can also compensate memory errors [6]. An effective way of reducing memory power is voltage scaling. About 35% powers saving is possible in the following JPEG2000 when memory operates at Scaled voltages [7]. This paper explains error control coding schemes such as adaptive Error control coding, single error correction double error detection (SECDED). The errors such as random errors and burst errors are replaced by these codes. These schemes are most suitable for SRAM [8].

For high performance JPEG2000 architecture, a QCB (quad code block)-based DWT method is proposed to achieve high parallelism in the JPEG2000 coprocessor for reducing the memory size [9]. In JPEG2000, if we process static images like pictures, Huffman encoding is enough. But if we operate the JPEG2000 for video transmission, Huffman-encoder is not sufficient because videos are transmitted at frames/sec. So if the clock frequency is high, then the dynamic images can be processed. For this case, JPEG based on entropy coder will be sufficient [1]. For JPEG2000 architecture, it uses an efficient 2-D DWT that is capable of computing four coefficients per clock cycle [10]. Memory is the main issue to store the number of images and videos. Here, we propose a technique called multiple-lifting scheme which can reduce not only average memory bandwidth but also 50% area of line buffer in 2-D DWT module [11].

Here we propose new pipelined architecture for JPEG2000. Based on pipelining process, it has efficient speed which is very important criteria to identify and remove SRAM errors. But the original system may be affected by total utilized logic elements with larger power consumption, high clock frequency and larger area yields larger power consumption. The normal architecture has resulted in a 20% decrease in hardware requirements



[12]. In JPEG2000, the most significant block is EBCOT which alone takes about 70% of the overall processing time for compression of image to compensate memory failures. The EBCOT is divided into two coding steps namely, Tier-1(binary arithmetic coding –MQ CODER) and Tier-2 (organization of the bit-stream) [13].Here we use high speed VLSI implementation of the EBCOT algorithm. The main concept is based on parallel access to memories which provides reduction in memory access requirements, as well as a net increase of processing speed by dramatically reducing the number of clock cycles [10].These techniques do not require any additional memory, have low circuit overhead, reduces the power with only a small reduction in image quality. In JPEG2000 architecture, the tile memory occupies more than 50% of area. Hence to solve this problem, we propose stripe pipeline scheme. Due to stripe-pipelining scheme, the initial level algorithms are changed as level switch discrete wavelet Transform (LS-DWT) and code block switch embedded block coding (CS-EBC). Because of this, the overall memory requirements can be reduced to only 8.5% comparing with conventional architecture [15].

2. RELATED WORK

A. SRAM failure analysis

In this paper, we analyse SRAM failures caused by voltage scaling. The voltage scaling is an effective way of reducing memory power. In JPEG2000 about 25% to 35% power saving is possible when the memory operates at scaled voltages [7].But voltage scaling introduces SRAM memory failures especially in scaled technologies. SRAM failure rate is affected by threshold voltage (V_t). SRAM failures include [16]:

1. Read stability failure (occurs during a read access, when current flows from the pre charged bit line).
2. Read latency failure (occurs during a read access, when the cell fails to pull down one of the bit lines).
3. Write latency failure (occurs during a write access, when the high voltage storage node cannot be pulled below the trip point).
4. Minimum hold voltage (occurs during the time when SRAM cell is not accessed).

The JPEG2000 can operate at low voltages used to store more data. It has high compression ratio but introduces memory failures due to low voltage operation. The three main factors that contribute overall SRAM failure rates [17]:

- i) Read upset - occurs during read cycles because of unbalanced voltage sharing at the read node.
- ii) Write access - occurs due to high drop or increase in the read and write current.
- iii) Read access - occurs when the scaled voltage is dropped drastically.

To compensate memory errors we use algorithmic specific techniques such as DCT, IDCT in JPEG and DWT, IDWT in JPEG2000 [6].But JPEG2000 is effective in compensating memory errors. Here we also use proposed stripe pipelining scheme but cannot reduce SRAM errors [15].

B. JPEG summary

JPEG is the most widely used image compression standard in today's world. It has lesser compression performance than JPEG2000 but has high PSNR value than JPEG2000 [3]. Because of simple structure and ease of simple implementation, it is still very popular. Memory errors can be compensated in JPEG implementation. An algorithmic-specific technique such as 2D-DCT is used to mitigate SRAM errors caused by voltage scaling. The three main features include [1]:

- i) The number of sign extension bits is determined in the quantization step.
- ii) Two adjacent AC coefficients after zigzag scan have similar values .Hence this is the main feature for JPEG.
- iii) Coefficients corresponding to higher frequencies have lesser values.

The JPEG based image compression improves PSNR (peak signal to noise ratio) performance [17] but reduce less SRAM Errors than JPEG2000. This is widely used compression standard in today's world. In JPEG the buffer acts as a memory for data storage. The block diagram has shown in Fig.1.

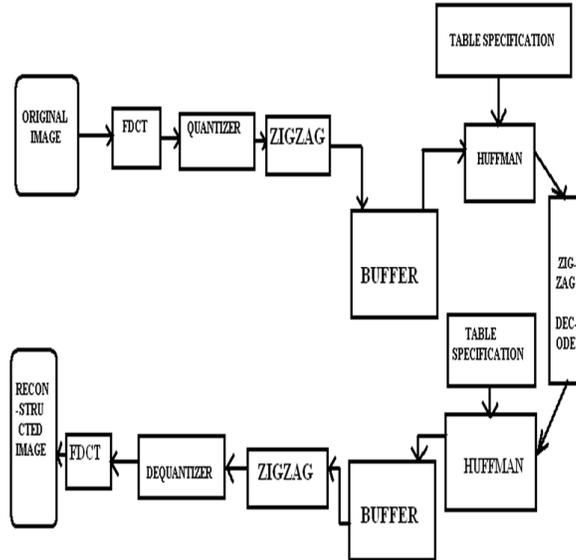


Fig.1.Block diagram of JPEG

In general, DC coefficient which is encoded in differential order by subtracting the DC coefficient of the previous block and encoding the difference using the Huffman table in baseline JPEG; the rest of the AC coefficients are encoded using another Huffman table. During Quantization, every coefficient in the 8*8 DCT matrixes is divided by the corresponding quantization value [6]. Zigzag scanning is used to order the 8*8 quantized coefficients into one dimensional vector in which low frequency coefficients are placed in front of high frequency coefficients [1]. The JPEG is a lossy coding method that results in some loss of details and unrecoverable distortion [6]. It has high PSNR value but lower compression ratio than JPEG2000.

C. JPEG2000

JPEG2000 is the latest still image compression standard developed by ISO/IEC JTC. Some of the features of JPEG2000 include: multiple resolution representation, region of interest coding. JPEG2000 has a much higher algorithmic complexity [13]. In JPEG2000, encoding is the main process. During the encoding process, an image is partitioned into data matrices called Tiles [12]. In JPEG2000, DWT is a sub band transform which transforms images from the spatial domain to the frequency domain [20]. The 2-D DWT decomposes a tile into LL, LH, HL and HH sub bands. Then LL band can be further, recursively decomposed into next resolution in a dyadic fashion [9]. As an example there-level DWT decomposition, which results in 10 sub bands has shown in Fig.2. [10]

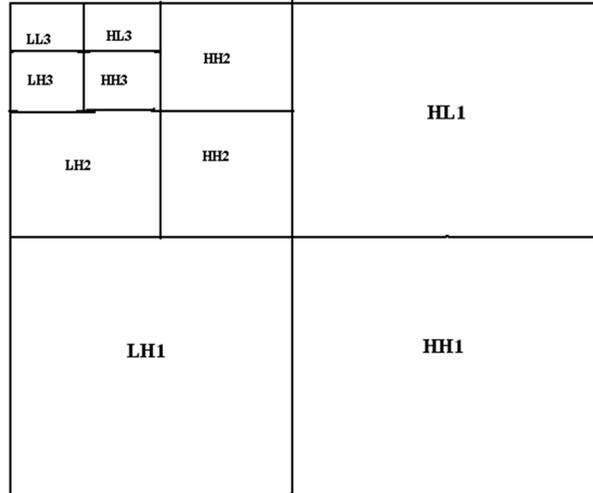


Fig.2.Three-level DWT decomposition of image tile

The process called quantization in which the sub band samples generated by the DWT are mapped onto the quantization indices for coding [12]. Generally, it is used to send in terms of coefficient values. In JPEG2000, the EBCOT is the main block which contains a larger computation time. In order to reduce computation time Tier-1 size is greatly reduced which uses context-based arithmetic coding to encode each code block into independent bit-stream [5]. EBCOT algorithm uses a wavelet transform to generate the sub band samples which are to be quantized. It uses Post-compression rate distortion optimization (PCRD-opt) algorithm for compensating SRAM errors in JPEG2000 [19]. The basic principle of EBCOT is: when coding, EBCOT receives a set of quantization coefficients together within a code block. To improve embedding, a fractional bit-plane coding method is used. Embedded coding, which is useful for scalability and for efficient rate control, is actually one of the main features of JPEG2000 [18]. Under this fractional coding method, one bit Plane is further decomposed into three passes according to coefficient’s significant situations. While scanning from the top bit plane, all-zero bit planes are skipped. EBCOT encodes each of the bit-plane in three coding pass. The three coding passes in the order in which they are performed on each bit Plane are significant propagation pass, magnitude refinement pass and clean up pass [11]. Each bit of the code-block is supported by one of these three passes, it sends data to MQ-pair to encode the bit has shown in Fig.3.

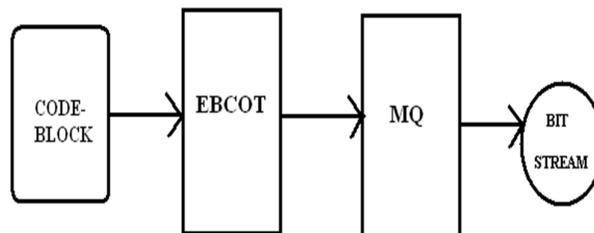


Fig.3.Information flowing between MQ-CODER and EBCOT

The adaptive binary arithmetic coder called MQ-CODER is used in JPEG2000 standard. The MQ-CODER utilizes a probability model for its encoding process. This model is implemented as a finite state machine (FSM) of 47 states. It consists of following algorithms: [12]

- i) CODEMPS algorithm (if most probable symbol has occurred, the CODEMPS algorithm is performed).
- ii) CODELPS algorithm (if least probable symbol has occurred, the CODELPS algorithm is performed).

Another significant block is rate control. Rate control is responsible for improving layer bit-rate targets. This can be achieved by two mechanisms:

- i) The choice of quantized step sizes.
- ii) The selection of subset of the coding to combine the code stream [13].

D. ADAPTIVE ECC

Here we use adaptive SECDED schemes where the stronger codes can be derived from weaker but longer codes. We use three different SECDED codes: (72, 64), (39, 32) and (22, 16). Among all these, SECDED codes (22, 16) is the strongest code with an area extension of 37.5% followed by (39, 32) with area extension of 21.9% and (22, 16) with an area increase of 12.5% [17]. The main aspect of these codes is that the parity generator matrix of the shorter code(stronger) can be derived from the parity generator matrix of the longer code(weaker).This can be utilized to design the hardware that can be shared for multiple codes. The parity generator matrix of (72, 64) with (39, 32) code consists of 8 rows (equal to number of parity bits).The first half of code (column 1 to 32) except the seventh row can be used to generate the parity Matrix of (39, 32) code since the seventh row consists of all zeros [17]. These adaptive error control coding schemes introduce little circuit overhead and no additional data storage is need for these codes. Similarly, the parity matrix of (22, 16) can be derived from the matrix of (39, 32) code by taking into an account the first 16 columns and dropping the all zero row.

Error correction code (ECC) techniques have been used to improve memory reliability. Especially, the extended Hamming and odd-weight column codes in the category of single error correction and double error detection (SEC-DED) codes are commonly used [8]. The overall bits computation is eliminated by check bit pre-computation during the write operation of memory despite using the error locator and double error detection code, which coincides with those of extended Hamming code.

3. RESULTS

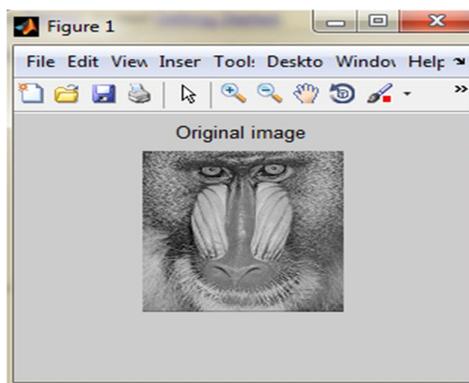


Fig 1.Original Image

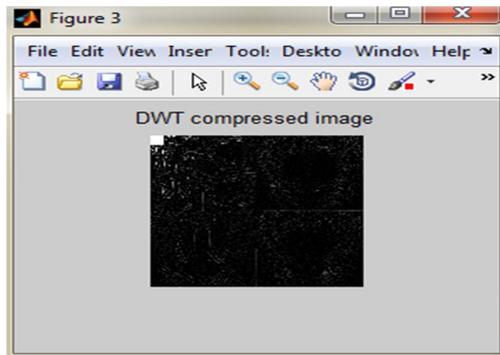


Fig 2. Image after DWT Compression

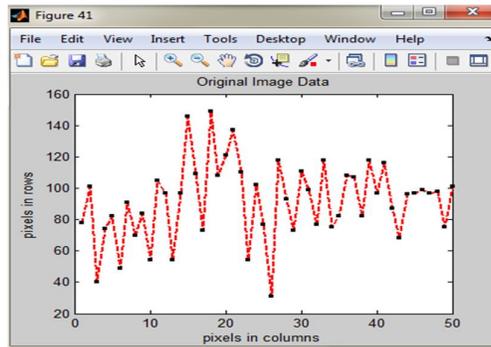


Fig 3. Graph of Original Image

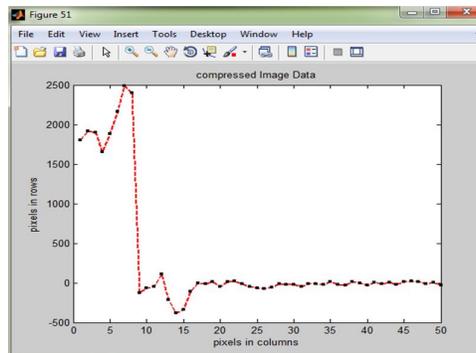


Fig 4. Graph of Compressed Image Date

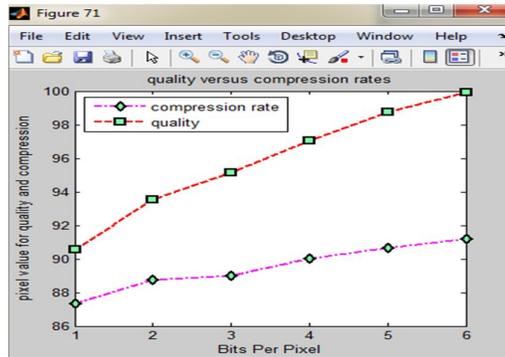


Fig 5.Performance Comparison

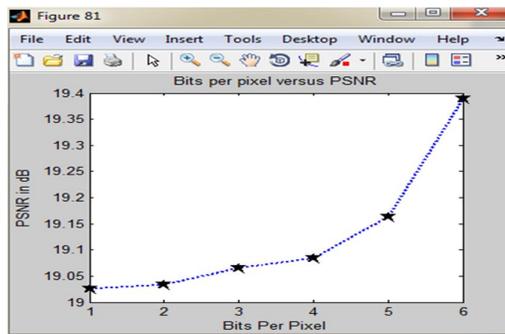


Fig 6.BPP vs. PSNR

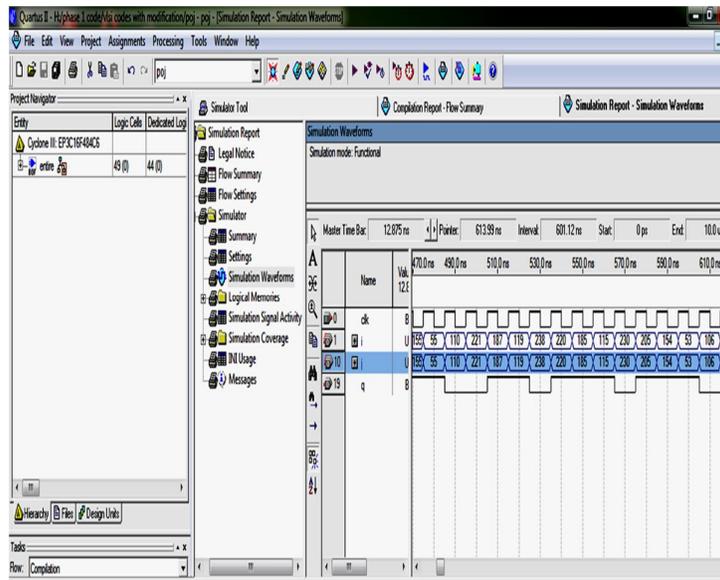


Fig 7.Output of MQ-CODER



4. CONCLUSIONS AND FUTURE WORK

In this paper, we presented various compression techniques such as DCT in JPEG and DWT in JPEG2000 for compensating memory errors. JPEG2000 is widely used as it outperforms JPEG. We also used Adaptive error control coding algorithm to mitigate memory failures caused by aggressive voltage scaling. Huffman coding is mainly used to compress more data. The proposed stripe pipeline scheme uses the LS-DWT and CS-EBC algorithm. These schemes do not require tile memory but can compensate SRAM errors having high working speed but having larger power consumption. Even though compression is done by the usage of Discrete Wavelet Transform, but it has less amount of reduction in image quality. Because in JPEG2000, there is some loss in quality of image. So SPIHT compression method can be used to increase the quality of the image in addition with Discrete Wavelet Transform which provides; high image quality and better compression ratio.

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